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Personal

Born and living in Verona, Italy.

Italian Citizen.

Education

Laurea specialistica Informatica (classe 23/S), Università degli Studi di Verona, Verona (Italy)

Thesis: "Studio di meccanismi di attacco e difesa delle reti wireless"

Supervisor: Prof. Roberto Segala

Co-examiner: Prof. Luca Viganò

Laurea triennale Informatica (classe 26), Università degli Studi di Verona, Verona (Italy)

Thesis: "Analisi di caratteristiche di linguaggi di specifiche algebriche: formalizzazione del modello di trasmissione di segnali biologici attraverso l'uso del linguaggio Maude"

Supervisor: Prof. G. Scollo

Degree of Academy of Music of Verona, agreement with "Centro Musicale San Marco", Verona Italy (Italy)

High school: Diploma di maturità tecnica industriale, I.T.I.S. "San Zeno", Verona (Italy)

Current position

Ph.D. Student 3rd year - Ph. D. Program in Computer Science

Università degli Studi di Verona - Dipartimento di Informatica (XIV cycle - January 01, 2009, December 31, 2011)

Ph.D. Advisor: Prof. Luca Viganò - Università degli Studi di Verona - Dipartimento di Informatica

Ph.D. Co-Advisor: Dr. Silvio Ranise - FBK - Security and Trust Unit - Trento (Italy)

Employment

Research assistant (Internship position)

Siemens AG - Corporate Technology, IT Security Department, Munich, Germany (March 01, 2011 - until now)

Research assistant

Università degli Studi di Verona - Dipartimento di Informatica (August 01, 2010 - until now)

Research assistant

Università degli Studi di Verona - Dipartimento di Informatica (August 01, 2008 - July 31, 2010)

Highschool teacher

I.T.I.S. "San Zeno", Verona (September 18, 2006 - June 30, 2008)

Research

Current research

Authorization aspects in security-sensitive services and applications

Access control logic and enforcement of access control and authorization policies in distributed systems

Decision procedures: building, combining, integrating, Satisfiability Modulo Theories (SMT) solvers

Automated Theorem Proving: rewriting, instantiation-based methods

Logic-based Program Verification

Model checking for infinite state systems

Projects and systems

Server-driven Outbound Web-application Sandboxing (**WebSand**)

European Project of FP7-ICT-STREP, (October 01, 2010 - September 30, 2013)

Research Consultant (March 01, 2011 - until now)

Secure Provision and Consumption in the Internet of Services (**SPaCioS**)

European Project of FP7-ICT-2009-5, Project No. 257876 (October 01, 2010 - September 30, 2013)

Researcher (August 01, 2010 - until now)

Automated VALidationN of Trust and Security of Service-oriented ARchitectures (**AVANTSSAR**)

European Project of FP7-ICT-2007-1, Project No. 216471 (January 01, 2008 - December 31, 2011)

Researcher (August 01, 2008 - December 31, 2010)

Web Service Solver Modulo Theory (WSSMT) - prototype (Java 1.5) - Eclipse plugin (v. 3.5)

A symbolic execution tool interfaced with satisfiability modulo theory solver.

Publications

International Journals

M.Barletta, A.Calvi, S.Ranise, L.Viganò, L.Zanetti. *Workflow and Access Control Reloaded: a Declarative Specification Framework for the Automated Analysis of Web Services*. - "Scalable Computing: Practice and Experience" Scientific International Journal for Paralle and Distributed Computing, Vol.12, N.1, 2011. pp.1-20, - SCPE 2011. Timisoara (Romania)

M.Barletta, S.Ranise, L.Viganò. *A Declarative Two-Level Framework to Specify and Verify Workflow and Authorization Policies in Service-Oriented Architectures*. - "Journal of Service Oriented Computing and Applications", Vol.5, N.1, pp.1-33, Springer 2010 - London

International Conferences

M.Barletta, J.Cuellar, S.Marchesini, S.Ranise, L.Viganò. *Access Control Framework for Secure Sensitive Services with Attribute-based Channel Communication* - in preparation

M.Barletta, J.Cuellar, S.Modersheim, L.Viganò. *Secure Attribute-based Channels* - in preparation

M.Barletta, S.Ranise, L.Viganò. *Symbolic Execution on Distributed System guided by Message Sequence Chart Representation* - to submit to CCS 2011

M.Barletta, S.Ranise, L.Viganò. *Verifying the Interplay of Authorization Policies and Workflow in Service-Oriented Architectures*. Proceedings of the 2009 International Symposium on Secure Computing (SecureCom 2009), Volume 3 of 2009 International Conference on Computational Science and Engineering (CSE 2009) - IEEE Computer Society Press

M.Barletta, S.Ranise, L.Viganò. *Verifying the Interplay of Authorization Policies and Workflow in Service-Oriented Architectures(Full version)*. CoRR, abs/0906.4570, 2009

International Workshops

M.Barletta, A.Calvi, S.Ranise, L.Viganò, L. Zanetti. *WSSMT: towards the automated analysis of Security-Sensitive Services and Applications* In Proc. of 1st Workshop on Software Service (satellite of SYNASC symposium), Timisoara, Sept. 23-25 (2010). To appear in IEEE Comp. Society, 2010.

Teaching

Software Engineering (Lab. Assistant)

Università degli Studi di Verona - Dipartimento di Informatica (Summer semester, 2011)

Software Engineering (Lab. Assistant)

Università degli Studi di Verona - Dipartimento di Informatica (Summer semester, 2010)